IN THE CLAIMS

Please cancel claims 1-2 and 5-16 without prejudice.

- 1-2. Canceled.
- 3. (Original) An input/output circuit for an integrated circuit comprising:
 - an input pad;
 - a pull-up circuit coupled to the input pad;
- a first multiplexer having a control terminal receiving values from a first plurality of memory cells, the first multiplexer having a first output signal coupled to a control terminal of the pull-up circuit;
 - a pull-down circuit coupled to the input pad;
- a second multiplexer having a control terminal receiving values from a second plurality of memory cells, the second multiplexer having a second output signal coupled to a control terminal of the pull-down circuit;
 - a tri-state buffer coupled to the input pad;
- a third multiplexer having a control terminal receiving a value from a first memory cell at a first input terminal and receiving a first user-defined value at a second input terminal, the third multiplexer having a third output signal coupled to an enable terminal of the tri-state buffer; and
- a fourth multiplexer having a control terminal receiving a value from a second memory cell at a first input terminal and receiving a second user-defined value at a second input terminal, the fourth multiplexer having a fourth output

signal coupled to a data output terminal of the tri-state buffer;

wherein a configuration signal from a user-defined logic circuit is provided to the control terminals of the first, second, third, and fourth multiplexers.

4. (Original) A method for an input/output circuit for an integrated circuit comprising: providing a pull-up circuit to an input pad of the input/output circuit; using a configuration signal to select between a first plurality of memory cells, thereby controlling the pull-up circuit;

providing a pull-down circuit to an input pad of the input/output circuit;
using the configuration signal to select between a second plurality of memory
cells, thereby controlling the pull-down circuit;

providing control signals to a tri-state buffer coupled to the input pad; and using the configuration signal to select between a plurality of signals used to centrol define the control signals.

5-16. Canceled.